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FAX COVER SHEET

May 27, 2003

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TO: Examiner James Mitchell
Art Unit 2827
United States Patent and Trademark Office

FAX number: (703) 305-3230 / *3432*
Voice number: (703) 305-0244

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MAY 27 2003

FROM: Steven R. Ormiston

TECHNOLOGY CENTER 2800

RE: Serial No. 09/834,696

PAGES: Number of pages, including this cover sheet: 5

ADDITIONAL MESSAGE:

Attached are the following documents for filing:

1. Amendment Transmittal Letter with a Certificate of Facsimile
2. Response to the Office Action mailed February 24, 2003

AMENDMENT TRANSMITTAL LETTER

Attorney Docket No:
MICR135.02Application Serial Number:
09/834,696Filing Date:
April 12, 2001Examiner:
J. MitchellGroup Art Unit:
2827

Invention: METHOD OF MAKING A SEMICONDUCTOR CHIP PACKAGE

TO THE COMMISSIONER OF PATENTS AND TRADEMARKS:

Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.

CLAIMS AS AMENDED

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	NO. OF EXTRA CLAIMS PRESENT	RATE	ADDITIONAL FEE
TOTAL CLAIMS	3	MINUS	20	0	\$18	\$ 0.00
INDEP. CLAIMS	3	MINUS	4	0	\$84	\$ 0.00

Petition is hereby made under 37 CFR 1.136(a) to extend the time for response to the Office Action of _____ to and through _____, comprising an extension of the shortened statutory period of:

one month (\$110) three months (\$920)
 two months (\$400) four months (\$1,440)

TOTAL ADDITIONAL FEE FOR THIS AMENDMENT	\$0.00
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- Small entity status of this application under 37 CFR 1.9 and 1.27 has been established by a verified statement previously submitted.
- A verified statement to establish small entity status under 37 CFR 1.9 and 1.27 is enclosed.
- A check in the amount of \$_____ is attached.
- Charge \$_____ to Deposit Account _____.
- No additional fee is required.

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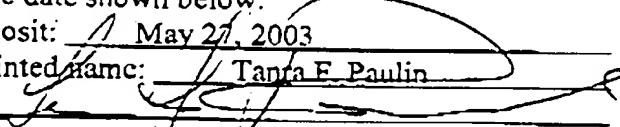
May 27, 2003

Date


 Steven R. Ormiston
 Reg. No. 35,974

TECHNOLOGY CENTER 2000

I hereby certify that this correspondence is being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below.

Date of Deposit: 1 May 27, 2003Typed or printed name: Tanya E. Paulin
 Signature: 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
Jerrold L. King and) Attorney
Jerry M. Brooks) Docket Number: MICR135.02
Serial No: 09/834,696) Group Art Unit: 2822
Filed: April 12, 2001) Examiner: J. Mitchell
For: SEMICONDUCTOR CHIP
PACKAGE)

)

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May 27, 2003

TECHNOLOGY CENTER 2800

Commissioner of Patents and Trademarks
Washington, DC 20231

Sir:

RESPONSE TO THE OFFICE ACTION MAILED FEBRUARY 24, 2003

REMARKS

Claims 19 and 23-24 are pending.

Rejection Under 35 U.S.C. § 102

Claim 19 was rejected under Section 102 as being anticipated by Ichiyama (5,373,190). In support of the rejection, the Examiner asserts that electrodes 8 or bumps 10 in Ichiyama are the conductive leads recited in Claim 19.

Claim 19 requires conductive leads electrically connected to and extending over a surface of the chip. Extend means "1. to stretch or draw out to full length.... 2. to stretch or draw outward. ... 4. to make longer, as to reach a particular point...." Random House Webster's College Dictionary (2000). For example, in the embodiment shown in Figs. 1-4, elongated leads 12 extend from near the center of chip 14 out past the edge of chip 14. By contrast, electrodes 8 and bumps 10 in Ichiyama do not extend